

E

UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

CONFIRMATION NO. 5713

Bib Data Sheet

DID Data Sheet									
SERIAL NUMBI 09/836,065	1 04/10/2001		CLASS 324		GROUP ART UNIT 2858			ATTORNEY DOCKET NO. P1310	
Janevoot Naksrikram, Bangkok, THAILAND; Aeksit Suraphak, Synnyvale, CA; Jitrayut Junnapart, Nonthaburi, THAILAND; ** CONTINUING DATA **********************************									
Foreign Priority claimed yes yes STATE OR SI					DRAV	EETS TOTA		MS	INDEPENDENT CLAIMS 2
ADDRESS Victor Flores LaRiviere, Grubman & Payne, LLP P.O. Box 3453 Monterey, CA 93942									
TITLE System and method for erase test of integrated circuit device having non-homogeneously sized sectors									
RECEIVED	FEES: Authority has been given in No to charge/credit DE No for following:			Paper POSIT ACCC	DUNT	All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue)			
						☐ Other			